

Inhalt

Supported Devices	1
WBM: Bridge configuration	1
Slave configuration	2
Order of connections	2
Configuration file for IP20	2
CODESYS	3

Supported Devices

- TP600
- CC100
- PFC200,300
- EC300

Not CP400, cause there is a need of 2 separate LAN Controllers.

WBM: Bridge configuration

Split from bridge to 2 separate ports, otherwise ethernet don't work.

Bridge Configuration

Port Mapping

Any change to the bridge configuration can interrupt the IP connection to the device.

Bridge	Port	
	X1	X2
1	☒	☐
2	☐	☒

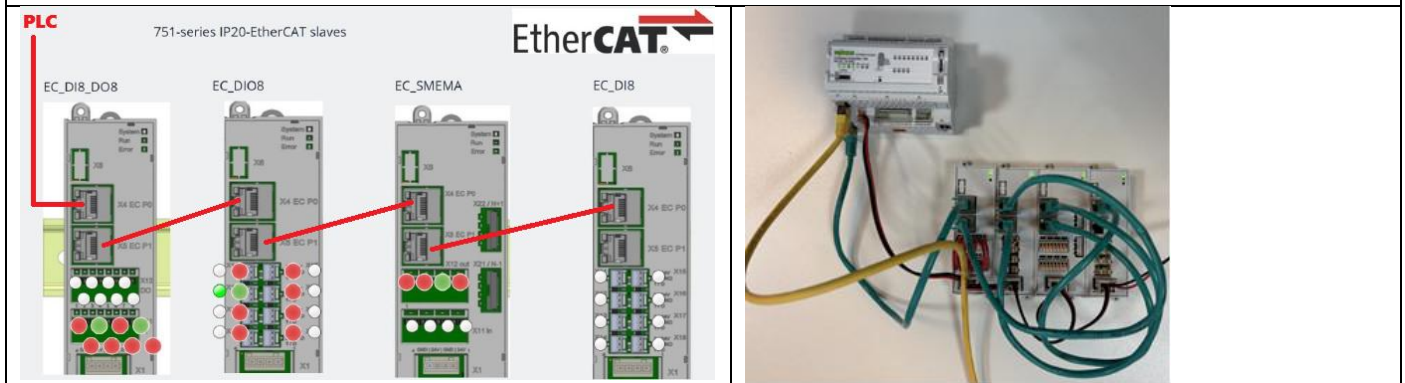
Submit

Slave configuration

Order of connections

The order how to connect ethernet is important. Please connect from one device to the next one.

Ethernet- Connection



Configuration file for IP20

Download the XML- file from here:

https://support.elrest.gmbh/PlcVisu/CC100/Elrest_IP20_EC_ASYX_XML.zip

CODESYS

I/O- mapping

The configuration should be shown as below.

General	Find	Filter	Show all	Add FB for IO Channel... Go to Instance				
Expert Process Data	Variable	Mapping	Channel	Address	Type	Default Value	Unit	Description
Process Data	16 #1600 DO RxPDO-Map Outputs		Outputs	%Q6	BYTE			Outputs
DC Settings	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[0]	Bit0		%Q6-0	BOOL			Outputs
Startup Parameters	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[1]	Bit1		%Q6-1	BOOL			Outputs
Log	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[2]	Bit2		%Q6-2	BOOL			Outputs
EtherCAT I/O Mapping	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[3]	Bit3		%Q6-3	BOOL			Outputs
EtherCAT IEC Objects	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[4]	Bit4		%Q6-4	BOOL			Outputs
Status	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[5]	Bit5		%Q6-5	BOOL			Outputs
Information	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[6]	Bit6		%Q6-6	BOOL			Outputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Outputs[7]	Bit7		%Q6-7	BOOL			Outputs
	16 #1A00 DI TxPDO-Map Inputs		Inputs	%I10	BYTE			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[0]	Bit0		%I10-0	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[1]	Bit1		%I10-1	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[2]	Bit2		%I10-2	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[3]	Bit3		%I10-3	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[4]	Bit4		%I10-4	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[5]	Bit5		%I10-5	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[6]	Bit6		%I10-6	BOOL			Inputs
	Application.GVL_EtherCAT.EC_DI8_DO8_Inputs[7]	Bit7		%I10-7	BOOL			Inputs

- EtherCAT_Master (EtherCAT Master)
- IP20_EC_DI8_DO8_ASYS (IP20_EC_DI8_DO8_ASYS)
- IP20_EC_DIO8_ASYS (IP20_EC_DIO8_ASYS)
- IP20_EC_SMEMA_AMI_ASYS (IP20_EC_SMEMA_AMI_ASYS)
- IP20_EC_DI8_ASYS (IP20_EC_DI8_ASYS)